



XA-9625B  
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yasutoshi AIBARA et al.

Appln. No.: 10/817,042

Group Art Unit: 2878

Filed: April 5, 2004

Examiner: S. Allen

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND  
IMAGING SYSTEM

Allowed: August 24, 2004

Confirmation No.: 2239

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attn: Mail Stop Issue Fee

Sir:

The reasons for allowance do not precisely track the recitations of independent Claims 17 and 26. These claims are clearly allowable by virtue of the combinations of features recited therein.

Respectfully submitted,

NHS:sjk

By: Nelson H. Shapiro  
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